EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		345/100.CCLS. and (shift adj register adj nand$1)) and (holder) and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:46
		345/100.CCLS. and (shift adj register same ((include$6 or consist$6 or compris$6 or contain$6 or construct$6) adj nand$1)) and (hold$6) and and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:08
		shift$6 adj register$6 and consist$6 ·adj nand$1 and ((sample$6 adj hold$6 or S/H)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:15
		(shift$6 adj register$6 adj contain$6 adj nand$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:11
		display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND same (consist$6 same nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:46
L1	140794	shift same register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 15:28
L2	158101	shift$6 same register$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 15:28
L3	4336	((shift$6 same register$6 same (includ$6 or consist$6 or compris$6)) same nand or shift$6 same register$6 with nand)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 15:31
L4	3887	((shift$6 adj register$6 same (includ$6 or consist$6 or compris$6)) same nand or shift$6 adj register$6 with nand)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 15:32

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L5	3185	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 15:33
L6	1352	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 15:34
L7	10	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and clock$6 and transister$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:14
L8	1	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and clock$6 and transister$1 and feed$6 same back	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:15
L9	4	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and clock$6 and transister$1 and (feed$6 same back or feedback$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:16
L10	151	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and (feed$6 same back or feedback$6) same nand same input$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:17
L11	145	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and (feed$6 same back or feedback$6) same nand same input$6 and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:18
L12	0	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and (feed$6 same back or feedback$6) same nand same input$6 and clock$6 and transister$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:19
L13	0	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and (feed$6 same back or feedback$6) same nand same input$6 and transister$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:19

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L14	112	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and (feed$6 same back or feedback$6) same nand same input$6 and transistor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:20
L15	107	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and (feed$6 adj back or feedback$6) same nand same input$6 and transistor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:21
L16	2	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and (feed$6 adj back or feedback$6) adj nand same input$6 and transistor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:21
L17	0	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and (feed$6 adj back or feedback$6) adj nand adj input$6 and transistor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:21
L18	1307	nand same form$6 same shift$6 same register$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:27
L19	0	nand adj form$6 adj shift$6 adj register$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:26
L20	3185	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:26
L21	10	((shift$6 adj register$6 adj (includ$6 or consist$6 or compris$6)) adj nand or shift$6 adj register$6 with nand) and (hold$6) and clock$6 and transister$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:26
L22	0	nand same form$6 same shift$6 same register$6 same input$6 same hold$6 same output same feedabck$6 same nand same input$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:28

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L23	986	nand same form$6 same shift$6 same register$6 same input$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:29
L24	63	nand same form$6 same shift$6 same register$6 same input$6 same hold$6 same output$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:29
L25	0	nand same form$6 same shift$6 same register$6 same input$6 same hold$6 same output$6 same feedabck$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:30
L26	3	nand same form$6 same shift$6 same register$6 same input$6 same hold$6 same output$6 same feedback$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:30
L27	3	nand same form$6 same shift$6 same register$6 same input$6 same hold$6 same output$6 same feedback$6 same nand same input$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:32
L28	356	nand same form$6 same shift$6 same register$6 same output$6 and input$6 same hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:32
L29	356	(nand same form$6 same shift$6 same register$6 same output$6 and input$6 same hold$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:33
L30	314	(nand same form$6 same shift$6 adj register$6 same output$6 and input$6 same hold$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:33
L31	12	(nand same form$6 same shift$6 adj register$6 adj output$6 and input$6 same hold$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/25 17:34
S1	96	display and shift adj register same consist$6 same nand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:40

S2	73	display and shift adj register same consist$6 same nand same circuit$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:46
S3	3	display and shift adj register same consist$6 same nand same circuit$6 and NMOS and PMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:41
S4	3	display and shift adj register same consist$6 same nand same circuit$6 and NMOS and PMOS and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:41
S5	0	display and shift adj register same consist$6 same nand same circuit$6 and NMOS and PMOS and clock and power same supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:42
S6	0	display and shift adj register same consist$6 same nand same circuit$6 and NMOS and PMOS and clock and power$6 same supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:42
S7	0	display and (shift$6 adj register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6 and power$6 same supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:43
S8	0	display and (shift$6 same register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6 and power$6 same supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:44
S9	3	display and (shift$6 same register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:43
S10	0	display and (shift$6 same register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6 and power$6 and supply$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:44
S11	3	display and (shift$6 same register$6) same consist$6 same nand same circuit$6 and NMOS and PMOS and clock$6 and power$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2006/10/26 17:44

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S12	22	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:52
S13	585	Kazuhiro and NODA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:47
S14	107	Kazuhiro and NODA and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:47
S15	96536	Kazuhiro nera3 NODA and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:47
S16	73	Kazuhiro near3 NODA and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:48
S17	9	Kazuhiro near3 NODA and display and shift adj register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 13:28
S18	3	Kazuhiro near3 NODA and display and shift adj register and nand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:49
S19	9	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:54
S20	0	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and NMOS and PMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:52
S22	0	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and PMOS and NMOS and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:54

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S23	3	display and (shift adj register) same (consist$6 same nand same circuit$6) and PMOS and NMOS and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/26 17:54
S24	9	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 14:16
S25	3	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and clock$6 and odd and even	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 14:19
S26	2	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and clock$6 and odd and even and phase$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:41
S27	2	"20040164947"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:43
S28	1	"20040164947" and parallel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:18
S29	0	display and (shift adj register) same (consist$6 same nand same circuit$6) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:45
S30	1	"20040164947" and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:44
S31	28	display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:45
S32	0	display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (consist$6 same nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:46

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S33	28	display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:46
S34	5	display and (shift adj register) and "345"/$.ccls. and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 16:46
S35	50	Tachibana AND Toshikazu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:27
S36	64	Tachibana AND Toshikazu display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:29
S37	1	Tachibana AND Toshikazu AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:30
S38	1	Sakamaki AND Goro AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:30
S39	1	Endo AND Kazuya AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:30
S40	1	Iwasaki AND Yoshitaka AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:30
S41	2	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 AND (nand same circuit$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 17:31
S42	8	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:53

S43	0	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and rise$6 same circuit$6 same improve$6 and wave same shap$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:12
S44	14	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and wave same shap$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:16
S45	0	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and wave same shap$6 and nand same cicuit$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:12
S46	9	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and wave same shap$6 and nand same circuit$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:13
S47	7	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and wave adj shap$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:35
S48	0	345/100.CCLS. AND display and (shift adj register) and hold$6 and clock$6 and rise adj time adj improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:35
S49	0	display and (shift adj register) and hold$6 and clock$6 and rise adj time adj improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:36
S50	0	display and (shift adj register) and hold$6 and clock$6 and rise$6 adj time adj improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:36
S51	5	display and (shift adj register) and rise$6 adj time adj improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/30 18:36
S52	0	"6,686,899".pn. and hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 13:11

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S53	1	"6,670,944".pn. and hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 13:11
S54	8	Kazuhiro near3 NODA and display and shift adj register and hold$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 13:28
S55	2	345/100.CCLS. AND display and (shift adj register) and hold$6 adj circuit$6 and clock$6 and rise$6 same circuit$6 same improve$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 13:48
S57	3	display and (shift adj register) and (hold$6 adj circuit$6) same CMOS same inverter and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 14:02
S58	0	display and (shift adj register) and (hold$6 adj circuit$6) same CMOS same inverter and clock$6 and wave$6 same shape$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 14:03
S59	14	display and (shift adj register) and (hold$6 same circuit$6) same CMOS same inverter and clock$6 and wave$6 same shape$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 08:58
S60	13	display and (shift adj register same nand) and (hold$6 same circuit$6) same CMOS same inverter and clock$6 and wave$6 same shape$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 09:22
S61	15	display and (shift$6 same register$6 same include$6 same nand$1) and input$6 and (hold$6 same circuit$6) same include$6 same CMOS same inverter and output$6 and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:34
S62	1	("2002/0018513").URPN.	USPAT	OR	ON	2007/05/22 09:30
S63	9	("20020018513" | "3387606" | "4225748" | "5341083" | "5386196" | "6236119" | "6476520" | "6533283"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/05/22 09:30
S64	117801	"359"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 09:32

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S65	25433	"359"/$.ccls. and display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 09:32
S66	2	"359"/$.ccls. and display and (shift$6 same register$6 same include$6 same nand$1) and input$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 09:36
S67	3	"359"/$.ccls. and display and (shift$6 adj register$6 and include$6 same nand$1) and input$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 09:37
S68	1	display and shift$6 adj register$6 adj includ$6 adj nand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 10:24
S69	1	display and shift$6 adj register$6 adj includ$6 adj nand$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 09:39
S70	3	display and (shift adj register) and (hold$6 adj circuit$6) same CMOS same inverter and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 10:12
S71	0	display and (shift adj register) and (hold$6 adj circuit$6) same CMOS same inverter and clock$6 and nand	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 10:12
S72	3	display and (shift adj register) and (hold$6 adj circuit$6) same CMOS same inverter and clock$6 and (nand or "and")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 10:13
S73	2	"6,670,944".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 11:10
S74	2	"5,886,353".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 11:33

S75	541	display and (shift$6 same register$6 same include$6 same nand$1) and output$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:41
S76	163	display and (shift$6 same register$6 same include$6 same nand$1) and output$6 and (sample$6 and hold$6 or S/H)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:44
S77	163	display and ((shift$6 same register$6 same include$6 same nand$1) and output$6) and ((sample$6 and hold$6 or S/H) and output$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:45
S78	95	display and ((shift$6 same register$6 same include$6 same nand$1) and output$6) and ((sample$6 and hold$6 or S/H) same output$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:45
S79	85	display and ((shift$6 same register$6 same include$6 same nand$1) same output$6) and ((sample$6 and hold$6 or S/H) same output$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:46
S80	0	display and ((shift$6 same register$6 same include$6 same nand$1) same output$6) and ((sample$6 and hold$6 or S/H) same output$6 same feed$6 same back same nand$1 same second same input$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:48
S81	0	display and ((shift$6 same register$6 same include$6 same nand$1) same output$6) and ((sample$6 and hold$6 or S/H) same output$6 and feed$6 same back same nand$1 same second same input$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:49
S82	5	display and ((shift$6 same register$6 same include$6 same nand$1) same output$6) and ((sample$6 and hold$6 or S/H) same output$6 and (feed$6 same back or feedback) same nand$1 same second same input$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:49
S83	19	display and ((shift$6 same register$6 same include$6 same nand$1) same output$6) and ((sample$6 and hold$6 or S/H) same output$6 and (feed$6 same back or feedback) same nand$1 same input$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:01

S84	1	display and ((shift$6 same register$6 same include$6 same nand$1) same output$6) same ((sample$6 and hold$6 or S/H) same output$6 and (feed$6 same back or feedback) same nand$1 same input$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 15:53
S85	2	display and ((shift$6 adj register$6) same (include$6 adj nand$1) same output$6) and ((sample$6 and hold$6 or S/H) same output$6 and (feed$6 same back or feedback) same nand$1 same input$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:02
S86	2	Nand$1 adj shift$6 adj register$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:09
S87	0	(shift$6 adj register$6 adj consrtuct$6) with nand$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:11
S88	0	shift$6 adj register$6 adj contain$6 adj nand$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:18
S89	0	shift$6 adj register$6 adj consist$6 adj nand$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:13
S90	139	shift$6 adj register$6 and consist$6 adj nand$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:14
S91	4	shift$6 adj register$6 and consist$6 adj nand$1 and (sample$6 adj hold$6 or S/H)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:15
S92	8	(shift$6 adj register$6 same contain$6 adj nand$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:19

EAST Search History

S93	20	345/100.CCLS. and (shift adj register) and (shift$6 adj hold$6 or S/H) and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:54
S94	481	345/100.CCLS. and (shift adj register) and (hold$6) and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:55
S95	2	345/100.CCLS. and (shift adj register adj nand$1) and (hold$6) and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 16:56
S96	0	345/100.CCLS. and (shift adj register adj (include$6 or consist$6 or compris$6 or contain$6 or construct$6) adj nand$1) and (hold$6) and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:04
S97	4	345/100.CCLS. and (shift adj register same ((include$6 or consist$6 or compris$6 or contain$6 or construct$6) adj nand$1)) and (hold$6) and clock$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:08
S98	1	345/100.CCLS. and (shift adj register same ((include$6 or consist$6 or compris$6 or contain$6 or construct$6) adj nand$1)) and (hold$6) and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:45
S99	0	345/100.CCLS. and (shift adj register same ((include$6 or consist$6 or compris$6 or contain$6 or construct$6) adj nand$1)) and (holder) and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:46
S10 0	0	345/100.CCLS. and (shift adj register adj nand$1) and (holder) and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:46
S10 1	0	345/100.CCLS. and (shift adj register same include$6 same nand$1) and (holder) and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:47
S10 2	0	345/100.CCLS. and (shift adj register same include$6 same nand$1) and (holder) and feedback$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:47

S10 3	0	345/100.CCLS. and (shift adj register same include$6 same nand$1) and holder and feedback$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:47
S10 4	0	345/100.CCLS. and (shift same register same include$6 same nand$1) and holder and feedback$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/22 17:47